



PATENT APPLICATION

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Tatsuo ITO et al.

Group Art Unit: 2125

Application No.: 10/502,389

Examiner: M. VON BUHR

Filed: July 26, 2004

Docket No.: 120503

For: METHOD FOR MANUFACTURING
SEMICONDUCTOR WAFER, METHOD FOR
RECEIVING ORDER FOR MANUFACTURE
OF SEMICONDUCTOR WAFER, AND
SYSTEM FOR RECEIVING ORDER FOR
MANUFACTURE OF SEMICONDUCTOR
WAFER

MAIL STOP RCE

**LARGE ENTITY REQUEST FOR
CONTINUED EXAMINATION UNDER 37 C.F.R. §1.114**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. §1.114, Applicants hereby request continued examination.

☒ Applicants further request entry and consideration of the attached Amendment.

The above-identified application was filed on or after June 8, 1995. Thus, entry is proper under 37 C.F.R. §1.114(d).

Attached hereto is our check no. 186750 in the amount of ☒ \$790.00 as payment of the fees set forth in 37 C.F.R. §1.17(e). The Commissioner is hereby authorized to charge any additional fees or credit any overpayment associated with this communication to Deposit Account No. 15-0461. Two duplicate copies of this page are enclosed.

Respectfully submitted,

William P. Berridge
Registration No. 30,024

Daniel A. Tanner, III
Registration No. 54,734

11/29/2006 SZEWDIE1 00000026 10502389

01 FC:1801

790.00 OP

WPB:DAT/jam

Date: November 28, 2006

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